Title: CODE PROCESSING CIRCUIT
Inventor(s): Shinji ONO
Appl. No.: 10/743,497

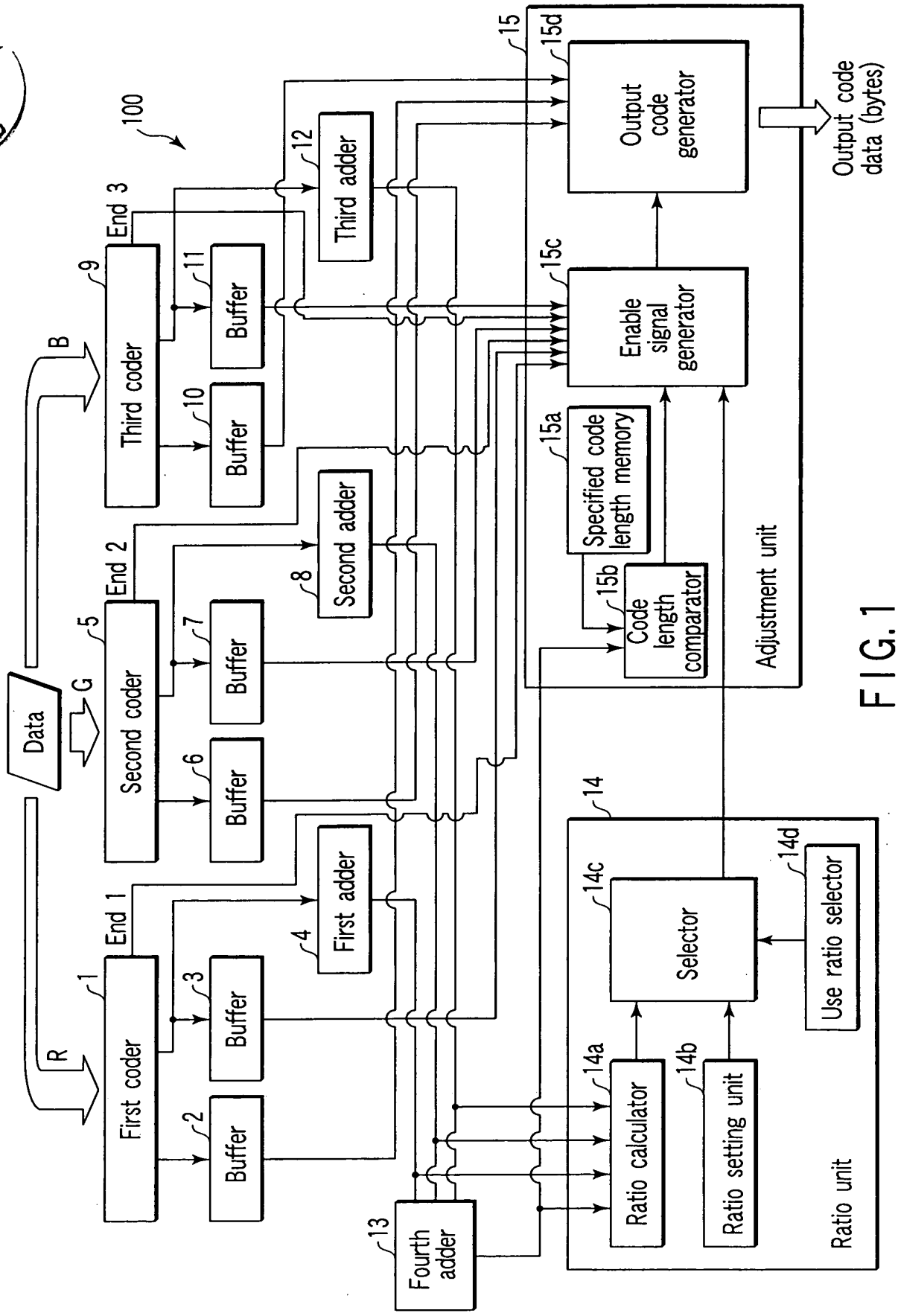

FIG. 1

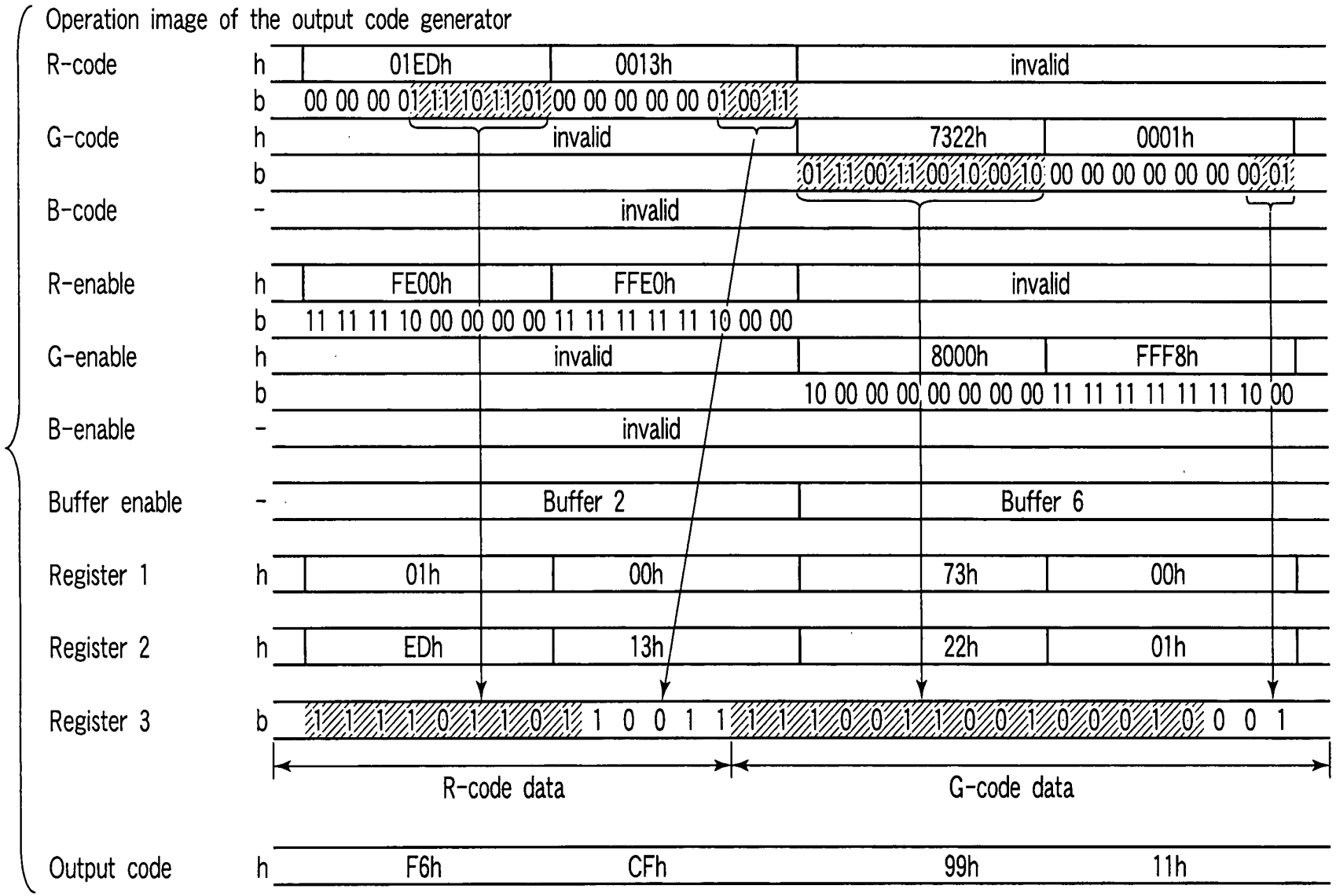

FIG. 2

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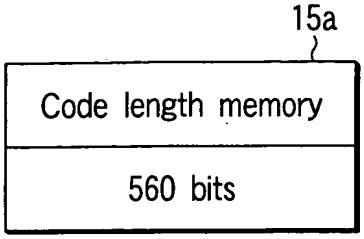

FIG. 3

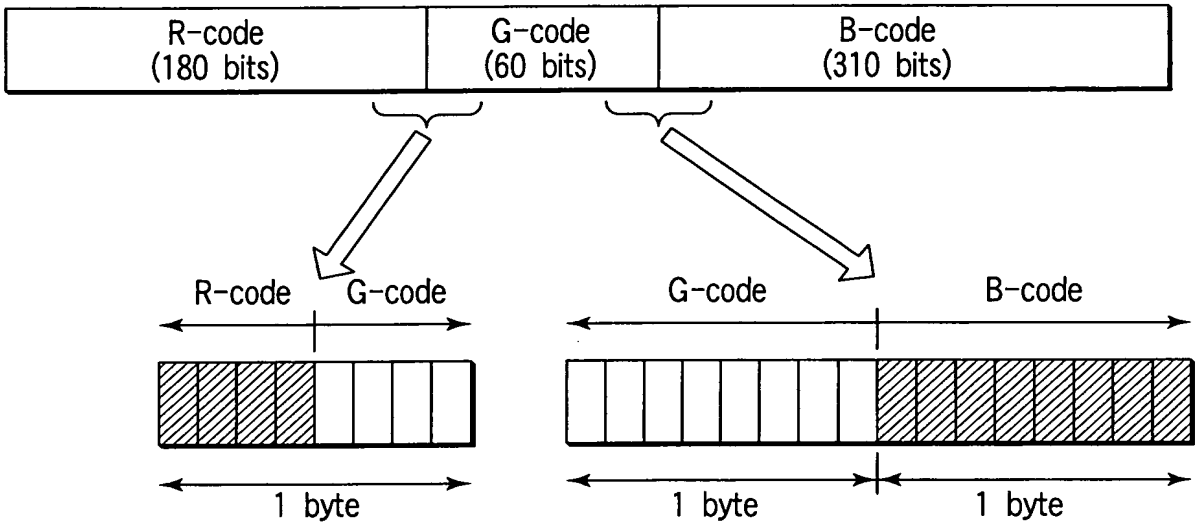

FIG. 4

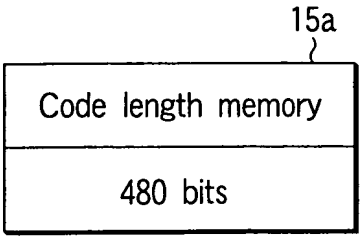

FIG. 5

	R-code (180 bits)	G-code (60 bits)	B-code (310 bits)
⋮	⋮	⋮	⋮
N-6th	7 bits	8 bits	9 bits
N-5th	4 bits	5 bits	12 bits
N-4th	6 bits	10 bits	5 bits
N-3rd	13 bits	3 bits	8 bits
N-2nd	9 bits	8 bits	4 bits
N-1st	4 bits	6 bits	8 bits
Nth	10 bits	7 bits	3 bits

FIG. 6

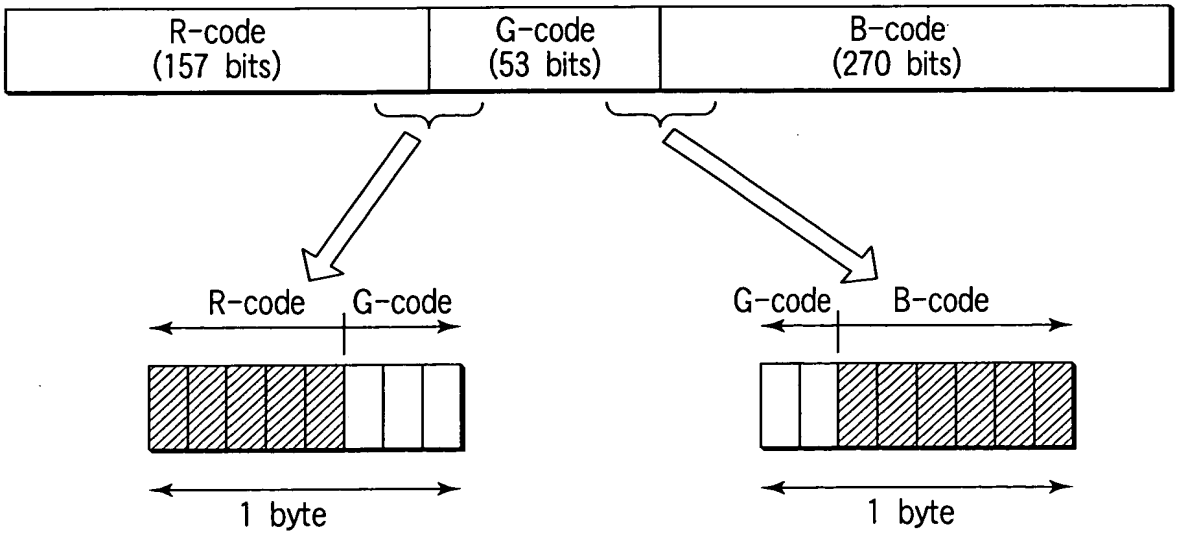

FIG. 7